

In the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims

1. (Currently Amended) A method for reducing the possibility of cold reset in a computer system that includes a central processing unit (CPU), a wake-up button that is used to awaken the CPU from a sleep mode, and a battery that supplies power to the computer system, the CPU supporting the function of software battery fault handling, and the method comprising-:

when the CPU is in the sleep mode and the computer system's power supply is in an uncertain status, the CPU staying in the sleep mode even a wake-up event occurs; and

when the CPU is in the sleep mode and the period during which the wake-up button is pressed is less than a predetermined value, the CPU continues to stay in the sleep mode, wherein the predetermined value is greater than the general value of the period during which the wake-up button is pressed due to a collision, an impact, or falling to the ground, and less than the general value of the period during which a user intentionally presses the wake-up button such that the computer system can be prevented damage by the collision, the impact, or falling to the ground.

2. (Original) The method according to claim 1, wherein the uncertain power supply status is the status of battery fault.

3. (Original) The method according to claim 1, wherein the uncertain power supply status is the status when the battery lid is opened.

4. (Original) The method according to claim 1, wherein the uncertain power supply status is the status when the battery is in low power.

5. (Original) The method according to claim 1, wherein the computer system is a personal digital assistant (PDA).

6. (Cancelled).

7. (Previously presented) The method according to claim 1, wherein the predetermined value is greater than 1~2 milliseconds and is less than 100 milliseconds.

8-12. (Cancelled)

13. (Currently Amended) A computer system comprising:
a wake-up button;
a CPU that is used to control the computer system and the CPU supports the function of software battery fault handling; and
a delay protection circuit that is used to detect the status of the wake-up button;

wherein when the computer system is in the sleep mode and the delay protection circuit has detected that the period during which the wake-up button is pressed is less than a predetermined value, then the CPU continues to stay in the sleep mode;

wherein the predetermined value is greater than the general value of the period during which the wake-up button is pressed due to a collision, an impact, or falling to the ground, and less than the general value of the period during which a user intentionally presses the wake-up button such that the computer system can be prevented damage by the collision, the impact, or falling to the ground.

14. (Original) The computer system according to claim 13, wherein when the computer system is in the sleep mode, the delay protection circuit is enabled; when the computer system is in normal operation mode, the delay protection circuit is disabled.

15. (Cancelled)

16. (Original) The computer system according to claim 13, wherein the computer system is a personal digital assistant (PDA).

17. (Previously presented) The computer system according to claim 13, wherein the predetermined value is greater than 1~2 milliseconds, and is less than 100 milliseconds.